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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	09/872,796	
	Filing Date	June 1, 2001	
	First Named Inventor	Craig L. Stevens	
	Art Unit	2813	
	Examiner Name	Kielin, Erik J.	
Total Number of Pages in This Submission	3	Attorney Docket Number	10001.000600 (NVLS 379)

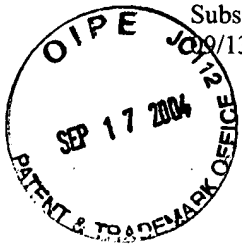
ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Patrick D. Benedicto, Reg No. 40,909 OKAMOTO & BENEDICTO LLP
Signature	<i>Patrick Benedicto</i>
Date	September 13, 2004

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09/13/2004



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Craig L. Stevens, et al.

Application No. 09/872,796

Examiner: Kielin, Erik J.

Filing Date: June 1, 2001

Art Unit: 2813

Title: Architecture For High Throughput Semiconductor Processing Applications

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Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUBSTANCE OF EXAMINER INTERVIEW**

Sir:

This submission is responsive to the Interview Summary mailed on August 17, 2004.

On August 16, 2004, the Examiner and the undersigned had a telephonic conference to discuss the restriction requirement mailed on July 30, 2004. The purpose of the interview is to identify ways to possibly examine all the pending claims in light of the relatively long prosecution history of this application. The Examiner suggested amending the pending claims so that they have similar scope. No agreement was reached on any of the pending claims.

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Docket No. 10001.000600 (NVLS 379)

Substance of Examiner Interview

09/13/2004

Respectfully submitted,  
Craig L. Stevens, et al.

Dated: 9/13/2004

By:

*Patrick D. Benedicto*

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Signature:	<i>Patrick D. Benedicto</i>		
Typed or Printed Name:	Patrick D. Benedicto	Dated:	<u>9/13/2004</u>